



Docket No. 251805US6

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: Tomohiro UEDA

SERIAL NO: 10/822,838

GAU: 2115

FILED: April 13, 2004

EXAMINER: SURYAWANSHI, S.

FOR: ELECTRONIC APPARATUS AND METHOD FOR CONTROLLING SEMICONDUCTOR  
INTEGRATED CIRCUIT

**REQUEST FOR EXTENSION OF TIME  
UNDER 37 C.F.R. 1.136**

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

It is hereby requested that a one month extension of time be granted to December 14, 2006 for

- ☒ filing a response to the Official Action dated: August 14, 2006.
- ☐ responding to the requirements in the Notice of Allowability dated:
- ☐ filing the Formal Drawings. The Issue Fee due has been timely filed.
- ☐ responding to the Notice to File Missing Parts of Application dated:
- ☐ filing a Notice of Appeal. A timely response to the final rejection, due has been filed.
- ☐ filing an Appeal Brief. A Notice of Appeal was filed on:
- ☐ Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee amount shown below is reduced by one-half.

The required fee of \$120.00 is enclosed herewith by credit card payment and any further charges may be made against the Attorney of Record's Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

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